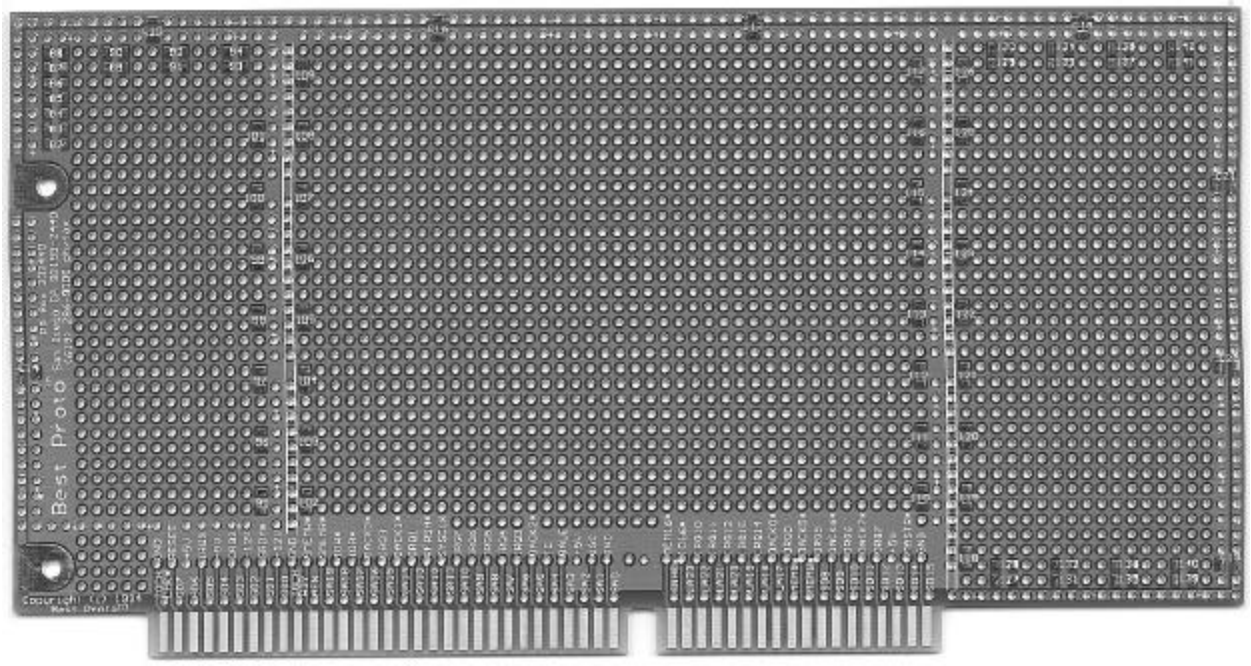
Jameco Part number 136469



COMPONET SIDE | SOLDER SIDE | P/N: 136469

COMPONENT SIDE
AND SOLDER SIDE
ARE THE SAME.

Jameco Part number 136469

ISA PROTOTYPING CARD
SIGNAL DEFINITIONS

BEST PROTO™
Box 232440
San Diego CA 92193-2440
(619) 286-9000 ph/fax

Signal Name	XT	AT	Signal Definition
BALE	√	√	Latch Address, AEN, SBHE*
SA<0-19>	√	√	Address first megabyte of memory and all I/O
LA<17-23>		√	Address 16 megabytes of memory
SBHE*		√	Valid data on SD<8-15>
AEN	√	√	Inactive = Access Cycle Active = Transfer Cycle
SD<0-7>	√	√	1st order data byte
SD<8-15>		√	2nd order data byte
SMEMR*, SMEMW*	√	√	First megabyte memory RD/WR
MEMR*, MEMW*		√	16 megabyte memory RD/WR
IOR*, IOW*	√	√	I/O RD/WR
MEMCS16*, IOCS16* (MEM16*, IO16*)		√	Accessed resource is 16 bits
IOCHRDY	√	√	Wait states needed
SRDY* (NOWS*)		√	Immediate cycle completion
REFRESH*	√	√	Refresh Cycle
MASTER16* (MASTR*)		√	16 bit ISA master
IOCHK*	√	√	Error on bus
RESET	√	√	Reinitialize bus resources
BCLK	√	√	4.77 Mhz on PC/XT, 6-8 Mhz on AT
OSC	√	√	14.31818 MHz
IRQx <3-7,9>	√	√	Interrupt request
IRQx <10-12, 14, 15>		√	Interrupt request
DACKx*, DRQx <1-3>	√	√	DMA & ISA Bus master request
DACKx*, DRQx <0,5-7>		√	DMA & ISA Bus master request
TC	√	√	DMA Termination - DMA to I/O resource

- Bus signal definitions Copyright (C) 1992, 1993 Annabooks, All Rights Reserved, reprinted here by special permission of Annabooks.

References for more information:

ISA & EISA Theory and Operation by Edward Solari, available from Annabooks, 11838 Bernardo Plaza Ct. #102, San Diego CA 92128, (619) 673-0870

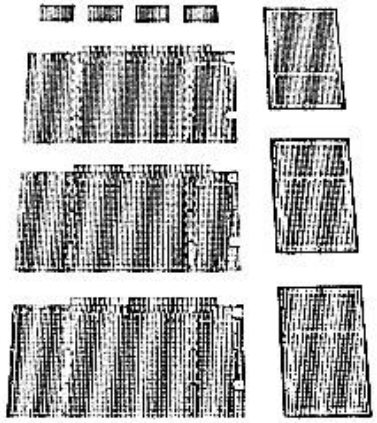
IEEE P996 PC Bus Standard Draft Specification v2.02 (1990) available from IEEE Standards Office, 445 Hoes Lane, Box 1331, Piscataway NJ 08855, (800) 678-4333 or Global Engineering, 18201 McDurmott West Ste. B, Irvine CA 92714, (800) 854-7179

Recycle

Jameco Part number 136469

Best Proto™

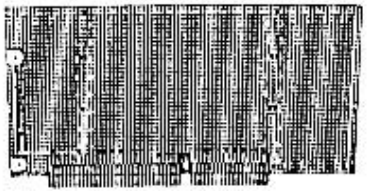
Electronics prototyping boards



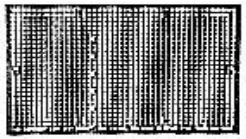
Engineer's Kit

Best Proto™ cards feature:

- Low noise power and ground planes
- Sites for SMD bypass capacitors and general purpose SMD passive components
- Silk-screen documentation on both sides of the board
- Low noise analog design areas and ample room for large PGA devices or wide header connectors.
- FR4 construction with all holes plated through and all edge connectors gold plated.



[SA/AT bus Best Proto™ card measures 4.2 "by 8.2" and can accommodate a PGA package with up to a 31x31 footprint. The board provides 21 predefined 1206 bypass capacitor sites and 142 unconnected 1206 device sites. **It includes a detailed data sheet describing bus signal functions**. An optional bracket provides additional mechanical support for internal PC bus slots.



Medium sized Best Proto™ card is 3" by 4.6" and can fit an PGA package with up to a 23x25 footprint. The board provides six pre-defined 1206 bypass capacitor sites, twenty one 1206 device sites, eleven 0805 device sites and eight additional 1206 sites **(G1-G8) with one terminal hard wired to the ground plane.**

Stamp sized Best Proto™ card. For prototyping single chip circuits. Provides a 1.2" x 0.6" available layout grid. Provides two 1206 bypass sites, 16 general purpose SMD sites, and two 0805 sites **(U1 and U18) with one terminal hard wired to the ground plane. Unconnected SMD terminals may be wired to a nearby through hole using a conductive pen.**

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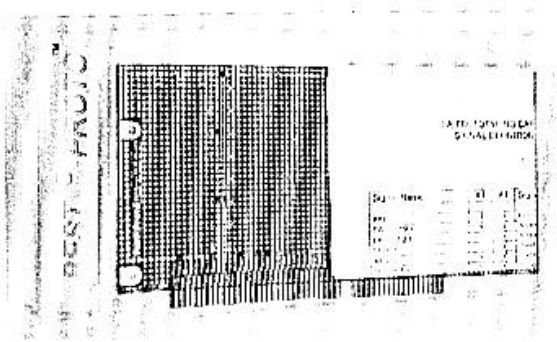
"*The best of these products that I have seen.*" Jeff B., Design Engineer, San Diego, CA.

"*It is very quiet because the grounding is very good. I like it*." - C. Narvaza, Engineer, San Diego, CA.

"*This board makes me want to turn on my soldering iron and build something*." - Philip Witham, Design Engineer, Vista, CA.



Retail ISA Package

We also offer products through retail channels. Ask your local electronics supply store to carry Best Proto™ equipment.

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